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| Form PTO-1449 | | ATTORNEY DOCKET NO. 10021250-01 | SERIAL NO. 10/814472 |
| <p style="text-align: center;">LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</p> <p style="text-align: center;">(Use several sheets if necessary)</p> | | APPLICANT Paul L. Corredoura, et al. | |
| | | FILING DATE March 29, 2004 | GROUP 2631 |

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | • | DOCUMENT NUMBER | DATE | NAME |
|---------------------|---|--------------------|---------------|---------------|
| /E.B./ | | US 2004/0056785 | Mar. 25, 2004 | Webster et al |
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FOREIGN PATENT DOCUMENT

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

| | |
|----------|---|
| /E.B./ | Johan Sommarek, Jouko Vankka, Jaakko Ketola, Ilari Teikari and Kari Halonen - "A Digital Quadrature Modulator With On-Chip D/A Converter", European Solid-State Circuits, 2003, ESSCIRC '03, Conference on Sept. 16-18, 2003, Piscataway, NJ, USA, IEEE; Pages 85-88. |
| /E.B./ | Basuki Endah Priyanto, Choi Look Law, Yong Liang Quan - "Design & Implementation Of All Digital I-Q Modulator And Demodulator For High Speed WLAN in FPGA"; 2003 IEEE Pacific Rim Conference on Communications, Computers And Signal Processing, Victoria, BC Canada, Aug. 28-30, 2003; IEEE Pacific Rim Conference and Communications, Computers And Signal Processing, NY; Pages 659-662. |
| /E.B./ | European Search Report dated August 4, 2005 |
| EXAMINER | Emmanuel Rovard/ |
| | DATE CONSIDERED |

* Copies of these references are not enclosed. Pursuant to 37 CFR 1.83(d). (See accompanying JPS.)

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